

ABSTRACT

A plasma display panel (PDP) includes front plate (4), scan electrodes (5) and sustain electrodes (6) both formed on front plate (4), dielectric layer (9) covering scan and sustain electrodes (5), (6), and protective layer (10) formed on dielectric layer (9). Protective layer (10) contains silicon (Si) and nitrogen (N), and is made of magnesium oxide (MgO) including Si of which atoms count in the range from 5×10^{18} pieces/cm³ to 2×10^{21} pieces/cm³, and N of which atoms count in the range from 1×10^{18} pieces/cm³ to 8×10^{21} pieces/cm³. The foregoing construction allows the PDP to shorten a discharge delay time, achieve a quick response of discharge to a voltage applied, and suppress changes of the discharge delay time with respect to a temperature.